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[Floating Gate Devices: Operation and Compact Modeling ...](#)

[Floating Gate Devices: Operation and Compact Modeling](#) Paolo Pavan * , Luca Larcher ** and Andrea Marmiroli *** * Dipartimento di Ingegneria dell ' In formazione, Universit à di Modena e Reggio Emilia,

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A floating gate and its application to memory devices Abstract: A structure has been proposed and fabricated in which semipermanent charge storage is possible. A floating gate is placed a small distance from an electron source. When an appropriately high field is applied through an outer gate, the floating gate charges up.

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A floating gate and a charge trap are types of semiconductor technology capable of holding an electrical charge in a flash memory device, but the chemical composition of their storage layers differs and they add and remove electrons in different ways.

What is floating gate transistor (FGT)? - Definition from ...

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Floating Gate devices are the basic building blocks of Semiconductor Nonvolatile Memories (EPROM, EEPROM, Flash). Among these, Flash are the most innovative and complex devices. The strategy

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followed developing this new model allows to cover a wide range of simulation conditions, making it very appealing for device physicists and circuit designers.

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The plasticity of vertical charge transfer in the MoS₂ floating gate device allows non-volatile conductance change under pulsed gate operation. This behaviour is analogous to biological synapses where the application of an excitatory or inhibitory pre-synaptic pulse has the effect of increasing or reducing the conductance of the synapse respectively.

A high-performance MoS₂ synaptic device with floating gate ...

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